TLVH431A-Q1 TLVH431B-Q1

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LOW-VOLTAGE ADJUSTABLE PRECISION SHUNT REGULATORS

Check for Samples: TLVH431A-Q1, TLVH431B-Q1

FEATURES

- Qualified for Automotive Applications
- Low-Voltage Operation: Down to 1.24 V
- Reference Voltage Tolerances at 25°C
 - 0.5% for B Grade
 - 1% for A Grade
- Adjustable Output Voltage, V_O = V_{REF} to 18 V
- Wide Operating Cathode Current Range: 100 μA to 70 mA
- 0.25-Ω Typical Output Impedance
- –40°C to 125°C Specifications

NC - No internal connection

* Pin 2 is attached to Substrate and must be connected to ANODE or left open.

DESCRIPTION/ORDERING INFORMATION

The TLVH431 devices are low-voltage 3-terminal adjustable voltage references, with thermal stability specified over the automotive temperature range. Output voltage can be set to any value between V_{REF} (1.24 V) and 18 V with two external resistors (see Figure 2). These devices operate from a lower voltage (1.24 V) than the widely used TL431 and TL1431 shunt-regulator references.

When used with an optocoupler, the TLVH431 devices are ideal voltage reference in isolated feedback circuits for 3-V to 3.3-V switching-mode power supplies. They have a typical output impedance of 0.25 Ω . Active output circuitry provides a very sharp turn-on characteristic, making the TLVH431 an excellent replacement for low-voltage Zener diodes in many applications, including on-board regulation and adjustable power supplies.

ORDERING INFORMATION(1)

T _A	V _{REF} TOLERANCE	PACKAGE ⁽²⁾		ORDERABLE PART NUMBER	TOP-SIDE MARKING	
-40°C to 125°C	0.5%	SOT-23-5 – DBV	Reel of 3000	TLVH431BQDBVRQ1	VOPQ	
	1%	SOT-23-5 – DBV	Reel of 3000	TLVH431AQDBVRQ1	VOOQ	

⁽¹⁾ For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.

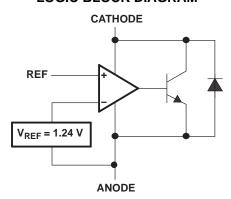
(2) Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.



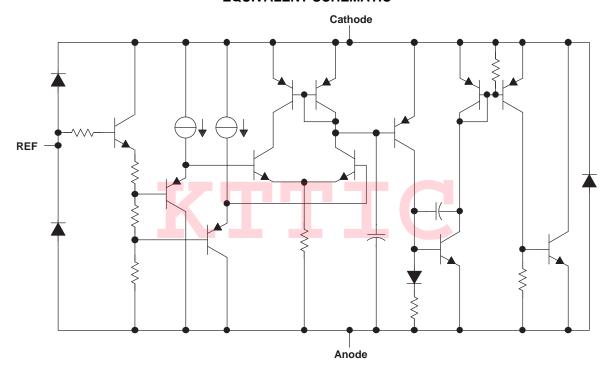
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LOGIC BLOCK DIAGRAM



EQUIVALENT SCHEMATIC





ABSOLUTE MAXIMUM RATINGS(1)

over operating free-air temperature range (unless otherwise noted)

V_{KA}	Cathode voltage ⁽²⁾	20 V
I _K	Cathode current range	–25 mA to 80 mA
I _{ref}	Reference current range	–0.05 mA to 3 mA
θ_{JA}	Package thermal impedance (3) (4)	206°C/W
T_J	Operating virtual junction temperature	150°C
T _{stg}	Storage temperature range	−65°C to 150°C

- (1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) Voltage values are with respect to the anode terminal, unless otherwise noted.
- (3) Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.
- (4) The package thermal impedance is calculated in accordance with JESD 51-7.

RECOMMENDED OPERATING CONDITIONS

		MIN	MAX	UNIT
V_{KA}	Cathode voltage	V_{REF}	18	V
I _K	Cathode current (continuous)	0.1	70	mA
T _A	Operating free-air temperature	-40	125	°C



Product Folder Link(s): TLVH431A-Q1 TLVH431B-Q1

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TLVH431A ELECTRICAL CHARACTERISTICS

at 25°C free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT
			T _A = 25°C	1.228	1.24	1.252	
V _{REF}	Reference voltage	$V_{KA} = V_{REF}$, $I_K = 10 \text{ mA}$	T _A = full range ⁽¹⁾ (see Figure 1)	1.209		1.271	V
V _{REF(dev)}	V_{REF} deviation over full temperature range $^{(1)}$ $^{(2)}$	V _{KA} = V _{REF} , I _K = 10 mA (see Figure 1)			11	31	mV
$\frac{\Delta V_{REF}}{\Delta V_{KA}}$	Ratio of $V_{\mbox{\scriptsize REF}}$ change to cathode voltage change	$V_K = V_{REF}$ to 18 V, $I_K = 10$ mA (see Figure 2)			-1.5	-2.7	mV/V
I _{ref}	Reference terminal current	$I_K = 10 \text{ mA}$, R1 = 10 k Ω , R2 = open (see Figure 2)			0.1	0.5	μΑ
I _{ref(dev)}	I _{ref} deviation over full temperature range ⁽¹⁾ (2)	I_K = 10 mA, R1 = 10 k Ω , R2 = open (see Figure 2)			0.15	0.5	μΑ
I _{K(min)}	Minimum cathode current for regulation	V _{KA} = V _{REF} (see Figure 1)			60	100	μΑ
I _{K(off)}	Off-state cathode current	V _{REF} = 0, V _{KA} = 18 V (see Figure 3)			0.02	0.1	μΑ
z _{KA}	Dynamic impedance (3)	$V_{KA} = V_{REF}$, f \leq 1 kHz, $I_K = 0.1$ mA to 70 mA (see Figure 1)			0.25	0.4	Ω

- (1) Full temperature range is -40°C to 125°C.
- (2) The deviation parameters V_{REF(dev)} and I_{ref(dev)} are defined as the differences between the maximum and minimum values obtained over the rated temperature range. The average full-range temperature coefficient of the reference input voltage, αV_{REF}, is defined as:

$$|\alpha V_{REF}| \left(\frac{ppm}{^{o}C}\right) = \frac{\left(\frac{V_{REF(dev)}}{V_{REF}\left(T_{A} = 25^{o}C\right)}\right) \times 10^{6}}{\Delta T_{\Delta}}$$

where ΔT_A is the rated operating free-air temperature range of the device.

aV_{REF} can be positive or negative, depending on whether minimum V_{REF} or maximum V_{REF}, respectively, occurs at the lower temperature.

(3) The dynamic impedance is defined as:

$$|z_{KA}| = \frac{\Delta V_{KA}}{\Delta I_{K}}$$

When the device is operating with two external resistors (see Figure 2), the total dynamic impedance of the circuit is defined as:

$$|z_{KA}| = \frac{\Delta V}{\Delta I} \approx |z_{KA}| \times (1 + \frac{R1}{R2})$$



TLVH431B ELECTRICAL CHARACTERISTICS

at 25°C free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT
			T _A = 25°C	1.234	1.24	1.246	
V _{REF}	Reference voltage	$V_{KA} = V_{REF}$, $I_K = 10 \text{ mA}$	T _A = full range ⁽¹⁾ (see Figure 1)	1.221		1.265	V
V _{REF(dev)}	V_{REF} deviation over full temperature range $^{(1)}$ $^{(2)}$	V _{KA} = V _{REF} , I _K = 10 mA (see Figure 1)			11	31	mV
$\frac{\Delta V_{REF}}{\Delta V_{KA}}$	Ratio of V_{REF} change to cathode voltage change	$I_K = 10 \text{ mA}, V_K = V_{REF} \text{ to } 18 \text{ V (see Figure 2)}$			-1.5	-2.7	mV/V
I _{ref}	Reference terminal current	$I_K = 10 \text{ mA}, R1 = 10 \text{ k}\Omega, R2 = \text{open (see Figure 2)}$			0.1	0.5	μА
I _{ref(dev)}	I_{ref} deviation over full temperature range ⁽¹⁾ (2)	$I_K = 10 \text{ mA}, R1 = 10 \text{ k}\Omega, R2 = \text{open (see Figure 2)}$			0.15	0.5	μΑ
I _{K(min)}	Minimum cathode current for regulation	V _{KA} = V _{REF} (see Figure 1)			60	100	μΑ
I _{K(off)}	Off-state cathode current	V _{REF} = 0, V _{KA} = 18 V (see Figure 3)			0.02	0.1	μΑ
z _{KA}	Dynamic impedance (3)	$V_{KA} = V_{REF}$, f ≤ 1 kHz, $I_K = 0.1$ mA to 70 mA (see Figure 1)			0.25	0.4	Ω

Full temperature range is -40°C to 125°C.

The deviation parameters $V_{REF(dev)}$ and $I_{ref(dev)}$ are defined as the differences between the maximum and minimum values obtained over the rated temperature range. The average full-range temperature coefficient of the reference input voltage, αV_{REF} , is defined as:

$$|\alpha V_{REF}| \left(\frac{ppm}{{}^{o}C}\right) = \frac{\left(\frac{V_{REF(dev)}}{V_{REF}\left(T_{A} = 25{}^{o}C\right)}\right) \times 10^{6}}{\Delta T_{A}}$$

where ΔT_A is the rated operating free-air temperature range of the device. αV_{REF} can be positive or negative, depending on whether minimum V_{REF} or maximum V_{REF} , respectively, occurs at the lower

temperature. The dynamic impedance is defined as:
$$\left|z_{KA}\right| = \frac{\Delta V_{KA}}{\Delta I_{K}}$$

When the device is operating with two external resistors (see Figure 2), the total dynamic impedance of the circuit is defined as:

$$|z_{KA}| = \frac{\Delta V}{\Delta I} \approx |z_{KA}| \times (1 + \frac{R1}{R2})$$



PARAMETER MEASUREMENT INFORMATION

Operation of the device at any conditions beyond those indicated under *recommended operating conditions* is not implied.

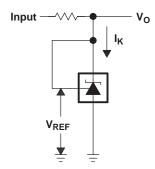


Figure 1. Test Circuit for $V_{KA} = V_{REF}$, $V_O = V_{KA} = V_{REF}$

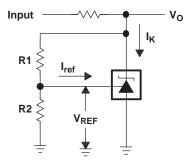


Figure 2. Test Circuit for $V_{KA} > V_{REF}$, $V_O = V_{KA} = V_{REF} \times (1 + R1/R2) + I_{ref} \times R1$

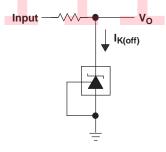
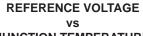


Figure 3. Test Circuit for I_{K(off)}





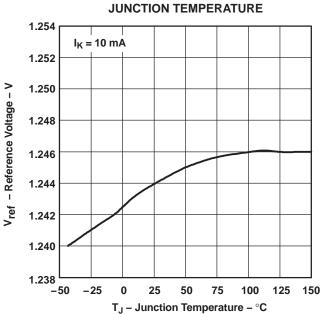


Figure 4.

REFERENCE INPUT CURRENT JUNCTION TEMPERATURE 250 I_K = 10 mA 230 $R1 = 10 \text{ k}\Omega$ 210 R2 = Open 190 170 150 130 110 90 70 50 -50 -25 0 25 50 75 100 125 150 Figure 5.



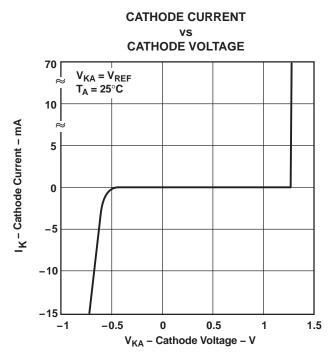


Figure 6.

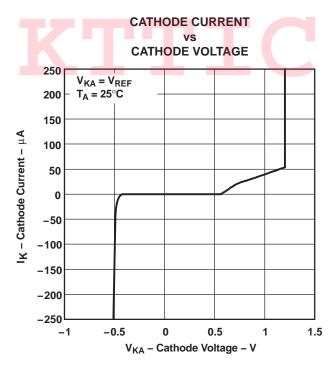


Figure 7.



OFF-STATE CATHODE CURRENT VS UNCTION TEMPERATURE

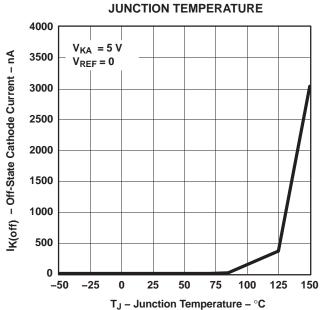


Figure 8.

TO DELTA CATHODE VOLTAGE

JUNCTION TEMPERATURE

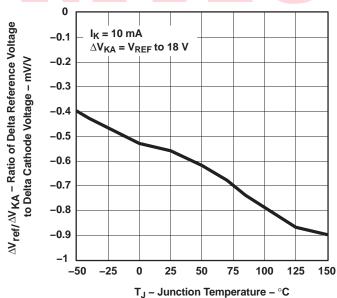


Figure 9.

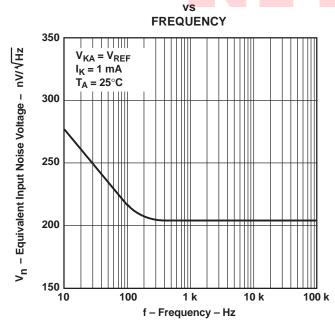


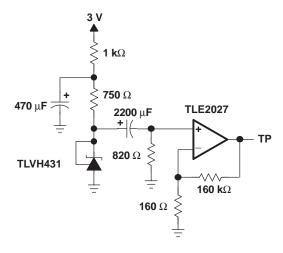
PERCENTAGE CHANGE IN V_{REF} **OPERATING LIFE AT 55°C** 0.025 I_K = 1 mA 0 Percentage Change in Vref - % % Change (avg) - 0.025 % Change (3 δ) - 0.05 -0.075-0.1% Change (-3δ) - 0.125 0 10 20 30 40 50 60 Operating Life at 55°C - kh(1)

(1) Extrapolated from life-test data taken at 125°C; the activation energy assumed is 0.7 eV.

Figure 10.

EQUIVALENT INPUT NOISE VOLTAGE



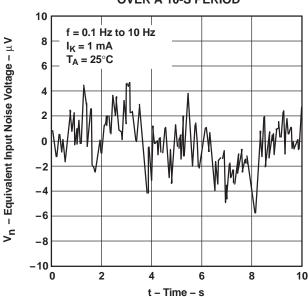


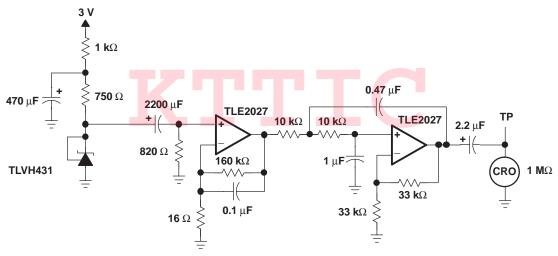
TEST CIRCUIT FOR EQUIVALENT INPUT NOISE VOLTAGE

Figure 11.



EQUIVALENT INPUT NOISE VOLTAGE OVER A 10-S PERIOD



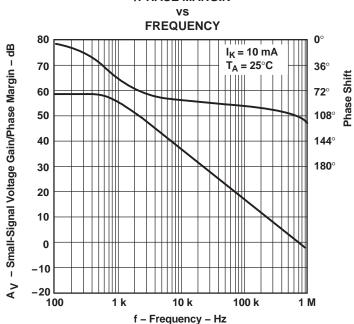


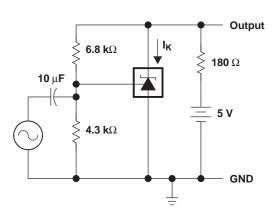
TEST CIRCUIT FOR 0.1-Hz TO 10-Hz EQUIVALENT NOISE VOLTAGE

Figure 12.



SMALL-SIGNAL VOLTAGE GAIN /PHASE MARGIN



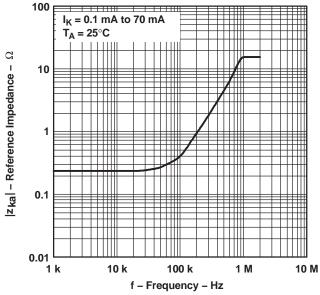


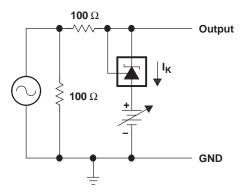
TEST CIRCUIT FOR VOLTAGE GAIN AND PHASE MARGIN

Figure 13.

REFERENCE IMPEDANCE vs



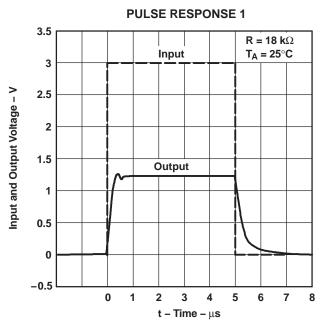




TEST CIRCUIT FOR REFERENCE IMPEDANCE

Figure 14.





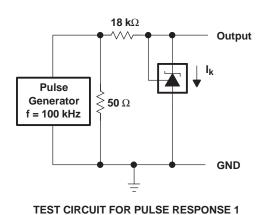


Figure 15.

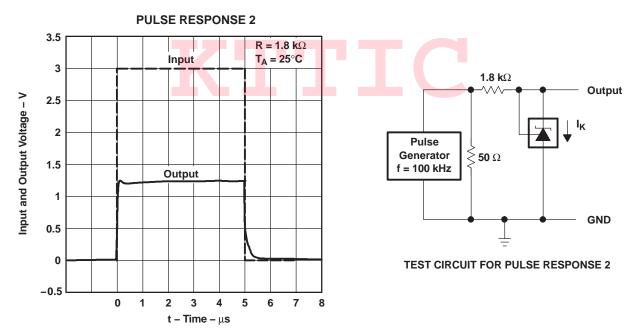


Figure 16.



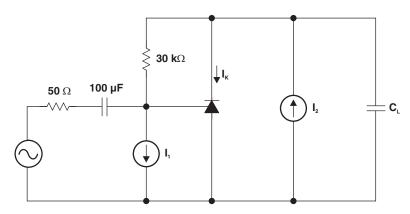


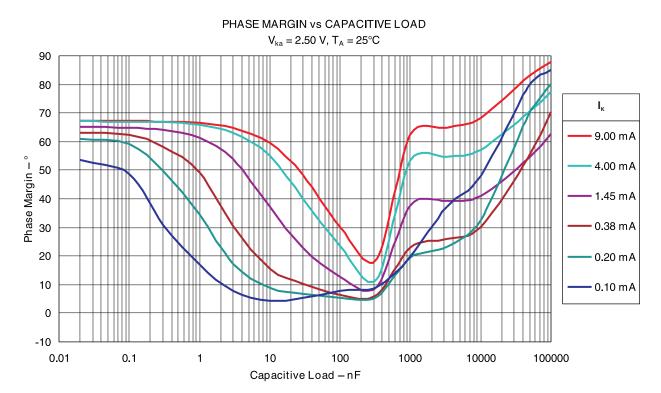
Figure 17. Phase Margin Test Circuit

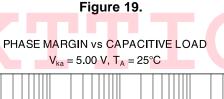
PHASE MARGIN vs CAPACITIVE LOAD $V_{ka} = V_{REF}$ (1.25 V), $T_A = 25^{\circ}C$ 90 80 I_{κ} 70 9.00 mA 60 Phase Margin – ° 4.00 mA 50 - 1.45 mA 40 30 0.38 mA 20 0.20 mA 10 - 0.10 mA 0 -10 0.01 0.1 1 10 100 1000 10000 100000

Figure 18.

Capacitive Load - nF







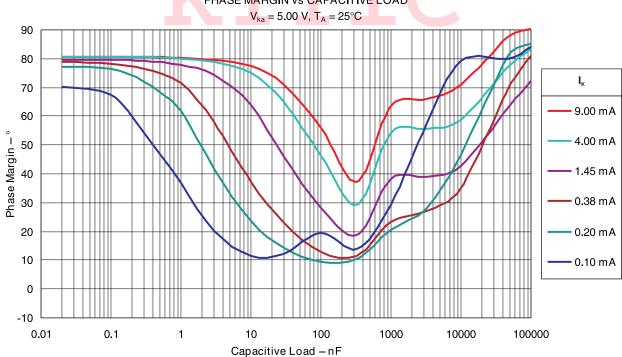


Figure 20.



APPLICATION INFORMATION

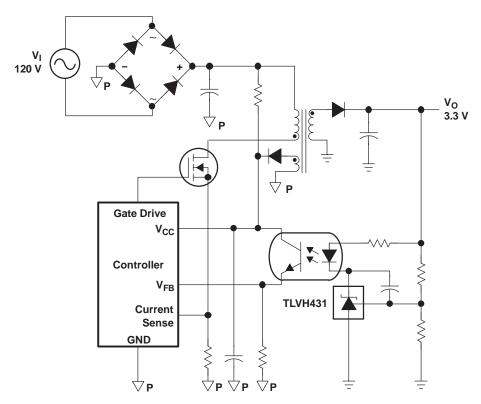


Figure 21. Flyback With Isolation Using TLVH431 as Voltage Reference and Error Amplifier

Figure 21 shows the TLVH431 used in a 3.3-V isolated flyback supply. Output voltage V_O can be as low as reference voltage V_{REF} (1.24 V). The output of the regulator plus the forward voltage drop of the optocoupler LED (1.24 + 1.4 = 2.64 V) determine the minimum voltage that can be regulated in an isolated supply configuration. Regulated voltage as low as 2.7 Vdc is possible in the topology shown in Figure 21.

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