

### EMIF02-MIC01F2

### 2-line IPAD™, EMI filter including ESD protection

#### **Features**

- 2-line symetrical low-pass filter
- Lead-free package
- High-density capacitor
- High-efficiency EMI filtering
- Very small PCB footprint: 1.42 mm x 1.92 mm
- Very thin package: 0.65 mm
- High-efficiency ESD suppression (IEC 61000-4-2 level 4)
- High reliability offered by monolithic integration

#### Complies with the standards:

- IEC61000-4-2 Level 4 on inputs and outputs
  - 15 kV (air discharge)
  - 8 kV (contact discharge)

#### **Application**

 Mobile phones (differential microphone filtering and ESD protection).

#### **Description**

The EMIF02-MIC01F2 is a highly integrated device designed to suppress EMI / RFI noise for microphone line filtering.

The EMIF02-MIC01F2 Flip Chip packaging means the package size is equal to the die size. This is why the EMIF02-MIC01F2 is a very small device.

Additionally, the filter includes an ESD protection circuit to prevent damage to the protected device when subjected to ESD surges up to 15 kV.

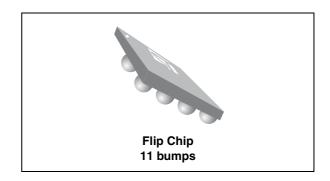


Figure 1. Pin configuration (bump side view)

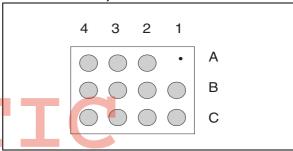
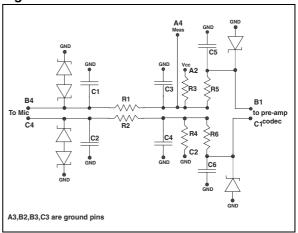


Figure 2. Schematic



TM: IPAD is a trademark of STMicroelectronics.

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Electrical characteristics EMIF02-MIC01F2

#### 1 Electrical characteristics

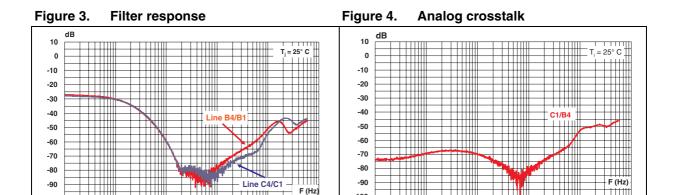
Table 1. Absolute maximum ratings ( $T_{amb} = 25$  °C)

Symbol	Parameter	Value	Unit
V	ESD IEC 61000-4-2, input and output pins - air discharge	15	kV
V <sub>PP</sub>	ESD IEC 61000-4-2, in put and output pins - contact discharge	8	K V
T <sub>j</sub>	Junction temperature	125	°C
T <sub>op</sub>	Operating temperature range	-40 to +85	°C
T <sub>stg</sub>	Storage temperature range	-55 to +150	°C

Table 2. Electrical characteristics ( $T_{amb} = 25$  °C)

Table 2.	Electrical characteristics (I <sub>amb</sub> = 25 °C)	1			
Symbol				<b>†</b> 1,	
V <sub>BR</sub>	Breakdown voltage				
I <sub>RM</sub>	Leakage current @ V <sub>RM</sub>				
V <sub>RM</sub>	Stand-off voltage	V <sub>CL</sub> V <sub>BR</sub>	V <sub>RM</sub>	I <sub>RM</sub>	V
V <sub>CL</sub>	Clamping voltage			I <sub>R</sub>	
R <sub>d</sub>	Dynamic impedance	slop	e : 1 / R <sub>d</sub>		
I <sub>PP</sub>	Peak pulse current			PP	
Symbol	Test condition	Min	Тур	Max	Unit
V <sub>BR</sub>	I <sub>R</sub> = 1 mA per line	14		18	V
V <sub>BR</sub>	I <sub>R</sub> = 1 mA per line V <sub>RM</sub> = 3 V per line	14		18 0.5	V µA
		0.8	1.0	_	-
I <sub>RM</sub> C1, C2, C3	V <sub>RM</sub> = 3 V per line		1.0	0.5	μA
I <sub>RM</sub> C1, C2, C3 C4, C5, C6	V <sub>RM</sub> = 3 V per line V <sub>LINE</sub> = 0 V, V <sub>OSC</sub> = 30 mV, F = 1 MHz			0.5	μA nF
I <sub>RM</sub> C1, C2, C3 C4, C5, C6 R1, R2	$V_{RM}$ = 3 V per line $V_{LINE}$ = 0 V, $V_{OSC}$ = 30 mV, F = 1 MHz Tolerance ± 5 %		50	0.5	μA nF

EMIF02-MIC01F2 **Electrical characteristics** 



1.0 G

100 M

-100

100 k

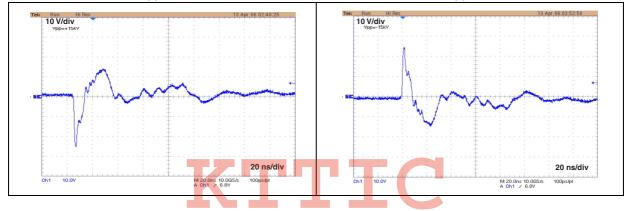
Figure 5. ESD response to IEC 61000-4-2 (+15 kV air discharge) on output (V<sub>OUT</sub>)

10.0 M

100 k

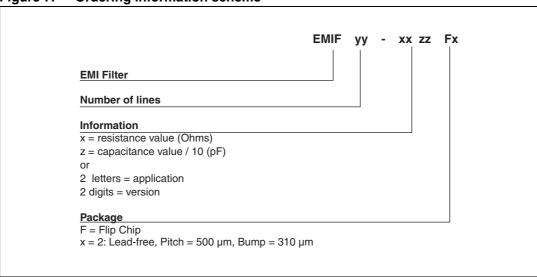
Figure 6. ESD response to IEC 61000-4-2 (-15 kV air discharge) on output (V<sub>OUT</sub>)

10 M



### 2 Ordering information scheme

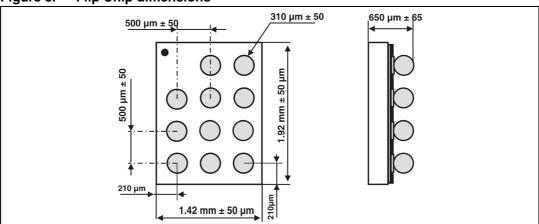
Figure 7. Ordering information scheme



### 3 Package information

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at www.st.com.

Figure 8. Flip Chip dimensions



EMIF02-MIC01F2 Ordering information

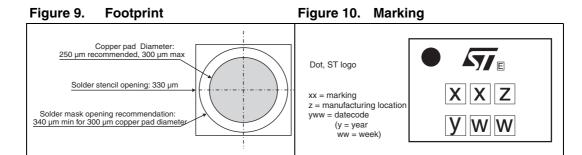
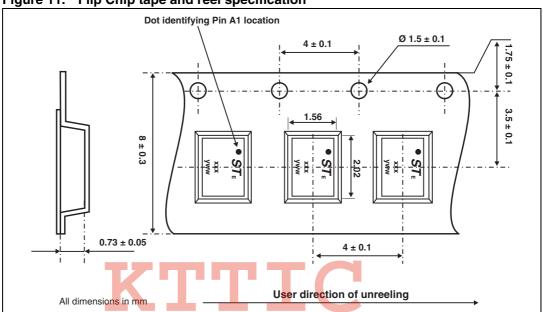


Figure 11. Flip Chip tape and reel specification



## 4 Ordering information

Table 3. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
EMIF02-MIC01F2	GB	Flip Chip	3.8 mg	5000	Tape and reel (7")

Note:

More packing information is available in the application notes:

AN1235: "Flip Chip: package description and recommendations for use"

AN 1751: "EMI filters: Recomendations and measurements"

Revision history EMIF02-MIC01F2

# 5 Revision history

Table 4. Document revision history

Date	Revision	Changes	
Sep-2004	3	Previous issue.	
09-Feb-2006	4	Added ECOPACK statement. Updated graphics to current standards.	
06-Oct-2006	5	Reformatted to current standards. Updated characteristic curves, removed Aplac information and updated tape and reel pocket dimensions.	
17-Apr-2008	6	Updated ECOPACK statement. Updated <i>Figure 7</i> , <i>Figure 8</i> and <i>Figure 11</i> . Reformatted to current standards.	



# KTTIC http://www.kttic.com

#### EMIF02-MIC01F2

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