

## Reliability and Qualification Report

# Silan BP2 Process Reliability Qualification using the SPX2815



Prepared By: Salvador Wu & Greg West

QA Engineering

Date: November 29, 2006

Reviewed By: Fred Claussen VP Quality & Reliability Date: November 29, 2006



#### **Table Of Contents**

Title Page	1
Table of Contents	2
Device Description	2
Pin Out	2
Manufacturing Information	2
Package Information	2
Reliability Test Summary	3
Life Test Data	3
FIT Data Calculations	4
MTBF Data Calculations	4
ESD Testing	4
3L TO-263 Pb Free Package Qualification Addendum	4

#### **Device Description:**

The SPX2815 is a low power positive-voltage regulator designed to satisfy moderate power requirements with a cost effective, small footprint solution. This device is an excellent choice for use in battery-powered applications and portable computers. The SPX2815 features very low quiescent current and a low dropout voltage of 1.1V at a full load. As output current decreases, quiescent current flows into the load, increasing efficiency. SPX2815 is available in adjustable or fixed 2.5V, 3.3V and 5.0V output voltages. The SPX2815 is offered in several 3-pin surface mount packages: TO-252, TO-220 and TO-263. An output capacitor of 10uF ceramic or tantalum provides unconditional stability.

## **Manufacturing Information:**

Product: SPX2815

Description: 1.5A NPN LDO Mask Set(s): MS1593AW

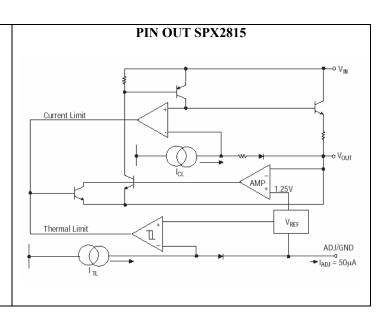
Lot Number(s): CF10025.1, CF10031.1,

CF10039.1

Process: sil-bp2 Wafer Fab: Silan

#### **Package Information:**

Package Type: TO-263-3L Package Code: JEDEC





#### **Reliability Qualification Test Summary:**

Stress Level	Device	Lot Number	Burn-In Temp	Sample Size	No. Fail
168Hrs	SPX2815	CF10025.1	125 °C	77	0
168Hrs	SPX2815	CF10031.1	125 °C	77	0
168Hrs	SPX2815	CF10039.1	125 °C	77	0
1000Hrs	SPX2815	CF10025.1	125 °C	77	0
1000Hrs	SPX2815	CF10031.1	125 °C	77	0
1000Hrs	SPX2815	CF10039.1	125 °C	77	0

#### Life Test

Life testing is conducted to determine if there are any fundamental reliability related failure mechanism(s) present in the device.

These failure mechanisms can be divided roughly into four groups:

- 1. Process or die related failures such as oxide defects, metallization defects, and diffusion defects.
- 2. Assembly related failures such as chip mount defects, wire bond defects, molding defects, and trim/form/singulation defects.
- 3. Design related defects.
- 4. Miscellaneous, undetermined, or application induced failures.

#### 125C Operating Life Test Results

As part of the Sipex design qualification program, the Product/Reliability Engineering group subjected 77 parts to 168 hours of 125° C life stress testing and then to 1000 hours of 125° C life stress testing.

### 168 Hour Timepoint

The 77 parts were subjected to the life test profile and completed the first phase with no failures.

#### 1000 Hour Timepoint

77 parts were reintroduced to life stress testing, completing the 1000 hour HTOL time point without any failures or significant shifts in process parameters.

#### **FIT Rate Calculations**

FIT rate (failures in time) is the predicted number of failures per billion device hours.

This predicted value is based upon,

- The Life Test conditions summarized in the HTOL table (time/temperature, device quantity, failure quantity).
- The Activation Energy (E<sub>a</sub>) for potential failure modes. The weighted Activation Energy(E<sub>a</sub>) of observed failure mechanisms for Sipex products has been determined to be 0.8eV.

http://www.kttic.com





Based on the above criteria SPX2815 product FIT rates for 25°, 55°, and 70°C of operation at 60% and 90% confidence levels have been calculated and listed below.

#### FIT Failure Rates: SPX2815 BP2 Silan Process

Confidence Level	+25°C	+55°C	+70°C
60%	1.8	29.5	99.2
90%	4.6	75.6	254.3

<sup>1</sup> FIT = 1 Failure per Billion Device-Hours

#### MTBF Calculation: SPX2815 BP2 Silan Process

Confidence Level	+25°C	+55°C	+70°C
60%	5.52E+08	3.39E+07	1.01E+07
90%	2.15E+08	1.32E+07	3.93E+06

#### **ESD Testing**

Human Body Model ESD - 36 units were subjected to Human Body Model ESD testing at +/- 2KV. All units passed.

## **Additional Reliability Tests**

77 of the units were placed on -65C/+150C Temperature Cycle testing, 77 of the units were placed on Highly-Accelerated Temp. and Humidity Stress testing (130C, 85% RH), 200 of the units were placed on ELFR testing and 77 on -65C/+150C Thermal Shock testing. All units passed testing as summarized in the following table.

Test	Condition	Time	Sample Size	# of rejects
TEMP. Cycles	-65C/+150C	1000 Cycles	77	0
HAST Unbiased	130C/85%RH	96hrs	77	0
ELFR	125C	48hrs	200	0
Thermal Shock	-65C/+150C	500 Cycles	77	0