

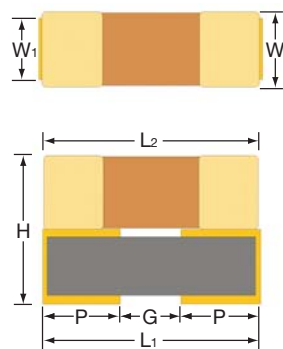
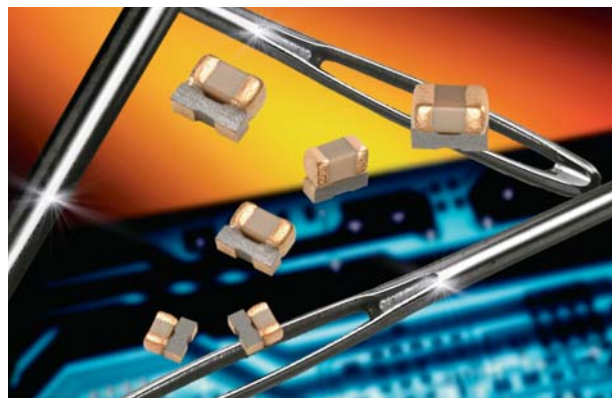
## Maxi Broadband DC Block to 40GHz

### GENERAL INFORMATION

The GZ Series was developed specifically to address DC Blocking issues from ~15KHz through 40GHz and incorporate small footprints to conserve board space. The three parts in this series are designed to match .015" (0.381mm) and .020" (0.508mm) micro strip widths. These assemblies (patent pending) combine high capacitance Ni-Au terminated MLC's for low frequency coverage and specially configured Maxi dielectric Single Layer Ceramics to facilitate conventional surface mounting. Most applications will experience resonance-free performance of <0.5dB thru at least 26.5GHz. Insertion loss at higher frequencies is in part dependent on installation parameters.

Custom designs (including those to match wider strip lines) are available upon request. Wire bondable designs for applications where strip line matching is not imperative, are also available. More information can be obtained by contacting the factory or your local AVX representative.

The assemblies are especially robust and capable of exceeding Resistance to Flexure Stress tests normally applied to MLC's. The high temperature connection between the MLC and SLC results in a joint whose re-melt temperature will withstand all normal soldering processes without deterioration. In applications where SN63 is used, the SLC termination will contribute <1% Au to the resultant joint—thereby, eliminating possible solder embrittlement problems.



### ELECTRICAL SPECIFICATIONS

PART NUMBER	GZ0415ZD104M600ZNW	GZ0402ZD104M800ZNW	GZ06023C104M800ZNW
<b>ELECTRICAL DATA</b>			
<b>Capacitance - MLC</b>	0.1 $\mu$ F $\pm$ 20%		
<b>Capacitance - SLC</b>	60 pF +80%, -20%	80 pF +80%, -20%	
<b>Voltage Rating</b>	16 VDC @ +85°C; 10 VDC @ +125°C		25 VDC @ 125°C
<b>Insulation Resistance</b>	10,000 MegOhms @ 25°C; 1,000 MegOhms @ 125°C		
<b>Operating Temp Range</b>	-55°C to +125°C		
<b>Temp Coefficient</b>	X5R to +85°C; X7S to +125°C		X7R to +125°C

<b>DIMENSIONS – Inches (Millimeters)</b>			
<b>L1 (SLC Length)</b>	.043 $\pm$ .003 (1.092 $\pm$ .076)		.063 $\pm$ .003 (1.600 $\pm$ .076)
<b>L2 (MLC Length)</b>	.040 $\pm$ .004 (1.016 $\pm$ .102)		.063 $\pm$ .003 (1.600 $\pm$ .076)
<b>W1 (SLC Width)</b>	.015 $\pm$ .002 (.381 $\pm$ .051)	.020 $\pm$ .002 (.508 $\pm$ .051)	
<b>W2 (MLC Max Width)</b>	.024 (.061)		.036 (.914)
<b>H (Overall Height)</b>	.046 (1.168) Max / .042 (1.067) Typ		.056 (1.422) Max / .053 (1.346) Typ
<b>G (Gap)</b>	.010 (.254) Typ		.020 (.508) Typ

<b>SLC PAD SIZE (P x W1 – Typ of 2)</b>	.015 $\pm$ .003 x .015 $\pm$ .002 (.381 $\pm$ .076 x .381 $\pm$ .051)	.015 $\pm$ .003 x .020 $\pm$ .002 (.381 $\pm$ .076 x .508 $\pm$ .051)	.020 $\pm$ .003 x .022 $\pm$ .002 (.508 $\pm$ .076 x .559 $\pm$ .051)
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<b>MTG FOOTPRINT (L1 x W1)</b>	.043 $\pm$ .003 x .015 $\pm$ .002 (1.092 $\pm$ .076 x .381 $\pm$ .051)	.043 $\pm$ .003 x .020 $\pm$ .002 (1.092 $\pm$ .076 x .508 $\pm$ .051)	.063 $\pm$ .003 x .022 $\pm$ .002 (1.600 $\pm$ .076 x .559 $\pm$ .051)
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<b>TERMINATIONS</b>	MLC – Ni-Au (plated); SLC – Ti/W-Ni-Au (sputtered)		
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<b>ATTACHMENT</b>	Conductive Epoxy and Sn/Pb, Au/Sn & SAC Alloys		
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<b>PACKAGING</b>	Waffle Pack (192 per) - 7" 8mm Tape & Reel Available (2000 pieces minimum)		
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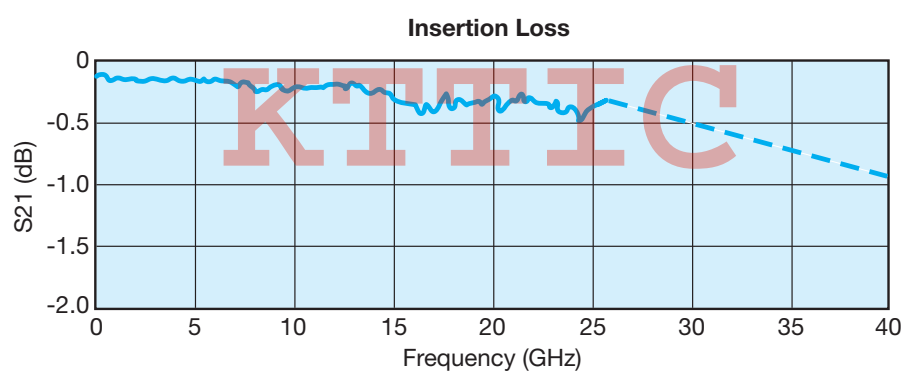
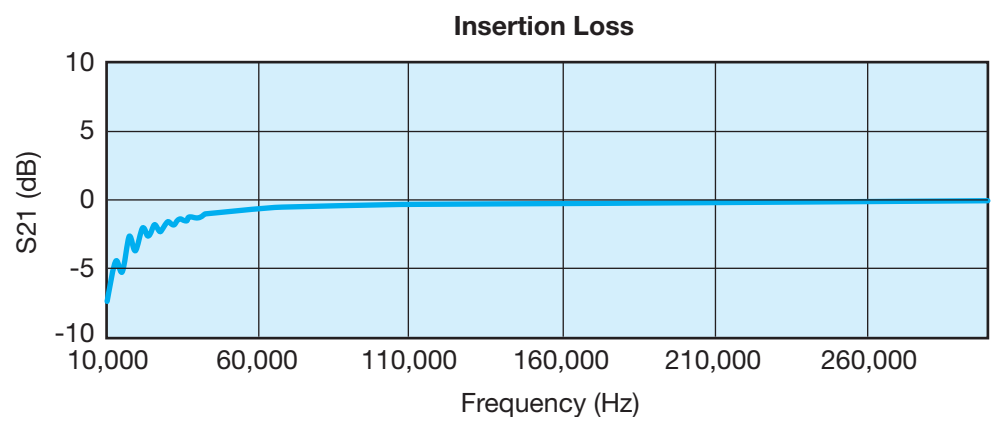


# GZ Series

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### HOW TO ORDER

<b>GZ</b>	<b>0402</b>	<b>Z</b>	<b>D</b>	<b>104</b>	<b>M</b>	<b>800</b>	<b>Z</b>	<b>N</b>	<b>W or T</b>
Style	Mounting Footprint	Voltage Rating Z = 10V Y = 16V 3 = 25V	MLC Dielectric X7R = C X5R = D	MLC Capacitance EIA Cap Code in pF	MLC Tolerance M = ±20%	SLC Capacitance EIA Cap Code in pF	SLC Tolerance Z = +80% -20%	SLC Termination Ti/W-Ni-Au	Packaging Code W = Waffle Pack T = Tape & 7" Reel



Typical performance on 10 mil alumina, 20 mil trace and 10 mil gap